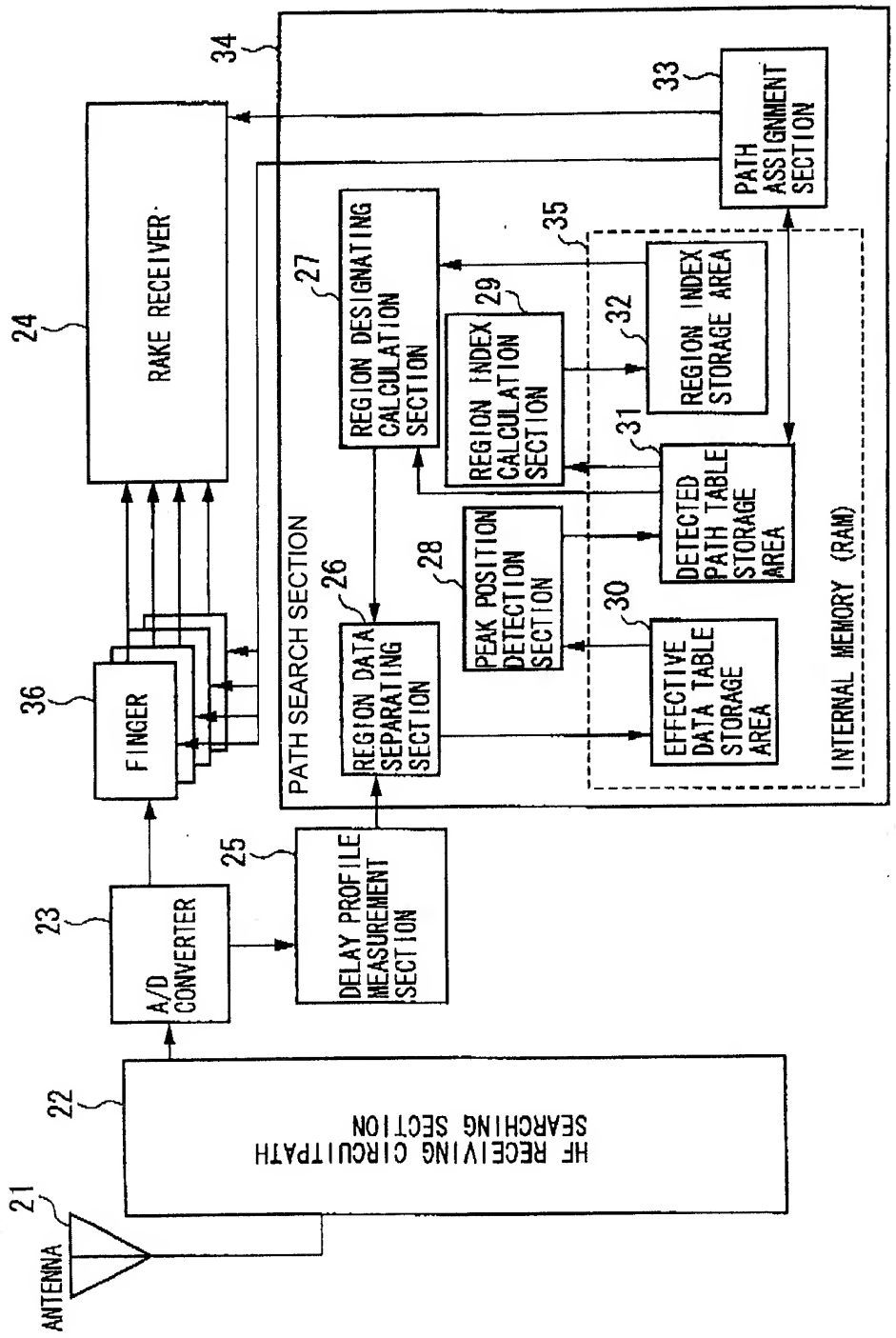
FIG. 1


ANTENNA
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HF RECEIVING CIRCUIT PATH SEARCHING SECTION
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A/D CONVERTER
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DELAY PROFILE MEASUREMENT SECTION
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FINGER
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RAKE RECEIVER
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PATH SEARCH SECTION
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REGION DATA SEPARATING SECTION
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PEAK POSITION DETECTION SECTION
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INTERNAL MEMORY (RAM)
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EFFECTIVE DATA TABLE STORAGE AREA
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